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U.S. UTILITY Patent Application

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APPLICATION NO. 09/179751	CONT/PRIOR	CLASS	SUBCLASS (OC)	ART UNIT	EXAMINER TO SO	2
E Tashio Yo	anada	-740		2129	Vo. Lilian	
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Semicenductor integrated circuit, computer system, data processor and data processing method

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CROSS REFERÊNCE(S)														
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	<u> </u>	DRAWINGS		CLAIMS ALLOWED			
TERMINAL DISCLAIMER	Sheets Drwg. Figs. Drwg.		Print Fig.	Total Claims	Print Claim for O.G.		
				NOTICE OF AL	LOWANCE MAILED		
The term of this patent subsequent to (date) has been disclaimed.	(Assistant	Examiner)	(Date)				
The term of this patent shall not extend beyond the expiration date				ISSUE FEE			
of U.S Patent. No.				Amount Due	Date Paid		
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